

1       CLAIMS

2       We claim:

3       1.       An apparatus comprising:

4       descriptor logic, said apparatus for controlling flow of data between first and second data  
5       processing systems via a memory, said descriptor logic for generating a plurality of  
6       descriptors including a frame descriptor defining a data packet to be communicated  
7       between a location in the memory and the second data processing system, and

8       a pointer descriptor identifying the location in the memory; and

9       a descriptor table for storing the descriptors generated by the descriptor logic for access  
10      by the first and second data processing systems.

11      2.       An apparatus as claimed in claim 1, wherein the descriptor table is stored in the  
12      first data processing system.

13      3.       An apparatus as claimed in claim 1, wherein the descriptor table is stored in the  
14      second data processing system.

15      4.       An apparatus as claimed in claim 1, wherein the descriptor logic generates a  
16      branch descriptor comprising a link to another descriptor in the descriptor table.

17      5.       An apparatus as claimed in claim 4, wherein the descriptor table comprises a  
18      plurality of descriptor lists sequentially linked together via branch descriptors therein.

1       6.     An apparatus as claimed in claim 4, wherein the descriptor table comprises a  
2     cyclic descriptor list.

3       7.     An apparatus as claimed in claim 1, wherein the first data processing system  
4     comprises a host computer system.

5       8.     An apparatus as claimed in claim 1, wherein the second data processing system  
6     comprises a data communications interface for communicating data between the host  
7     computer system and a data communications network.

8       9.     A data processing system comprising:

9     a host processing system having a memory, a data communications interface for  
10    communicating data between the host computer system and a data communications  
11    network, and

12    apparatus as claimed in claim 1, for controlling flow of data between the memory of the  
13    host computer system and the data communications interface

14    10.    A method comprising controlling flow of data between first and second data  
15    processing systems via a memory, the step of controlling comprising:

16    by descriptor logic, generating a plurality of descriptors including a frame descriptor  
17    defining a data packet to be communicated between a location in the memory and the  
18    second data processing system,

19    a pointer descriptor identifying the location in the memory; and

1       storing the descriptors generated by the descriptor logic in a descriptor table for access by  
2       the first and second data processing systems.

3       11.      A method as claimed in claim 10, comprising storing the descriptor table in the  
4       first data processing system.

5       12.      A method as claimed in claim 10, comprising storing the descriptor table in the  
6       second data processing system.

7       13.      A method as claimed in claim 10, comprising, by the descriptor logic, generating  
8       a branch descriptor comprising a link to another descriptor in the descriptor table.

9       14.      A method as claimed in claim 13, comprising linking a plurality of descriptor lists  
10      together in series via branch descriptors to form the descriptor table.

11      15.      A method as claimed in claim 10, wherein the first data processing system  
12      comprises a host computer system.

13      16.      A method as claimed of claim 10, wherein the second data processing system  
14      comprises a data communications interface for communicating data between the host  
15      computer system and a data communications network.

16      17.      A computer program product comprising a computer usable medium having  
17      computer readable program code means embodied therein for causing control of flow of  
18      data between first and second data processing systems, the computer readable program  
19      code means in said computer program product comprising computer readable program  
20      code means for causing a computer to effect the functions of claim 1.

1       18. A computer program product comprising a computer usable medium having  
2 computer readable program code means embodied therein for causing data processing, the  
3 computer readable program code means in said computer program product comprising  
4 computer readable program code means for causing a computer to effect the functions of  
5 claim 9.

6       19. An article of manufacture comprising a computer usable medium having  
7 computer readable program code means embodied therein for causing control of flow of  
8 data between first and second data processing systems, the computer readable program  
9 code means in said article of manufacture comprising computer readable program code  
10 means for causing a computer to effect the steps of claim 10.

11      20. A program storage device readable by machine, tangibly embodying a program of  
12 instructions executable by the machine to perform method steps for controlling flow of  
13 data between first and second data processing systems, said method steps comprising the  
14 steps of claim 10.